

FIGURE 1
(PRIOR ART)

FIGURE 2

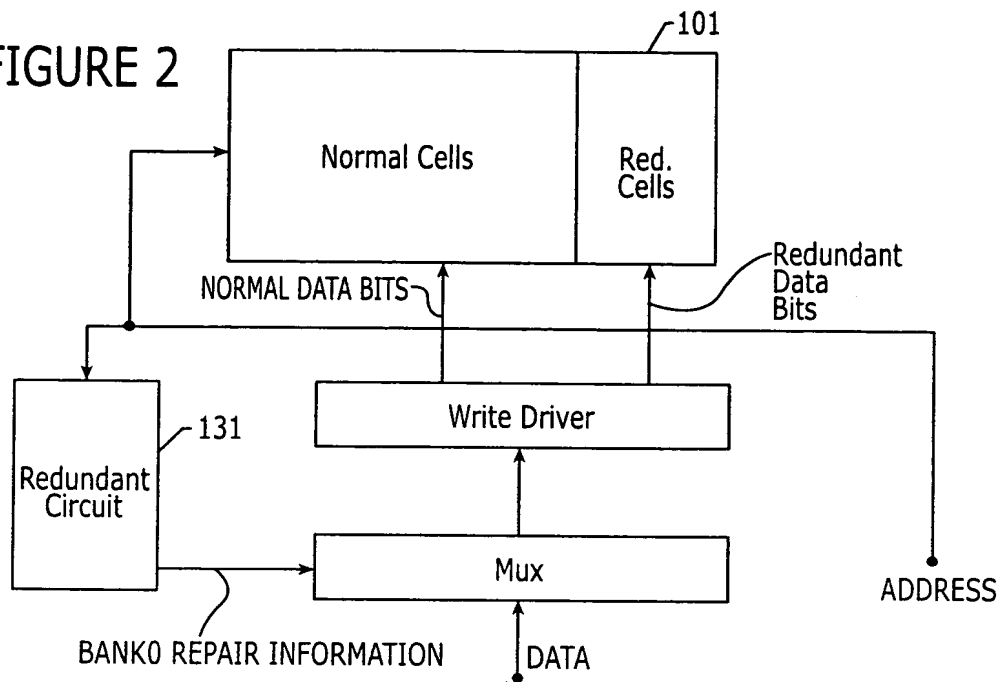
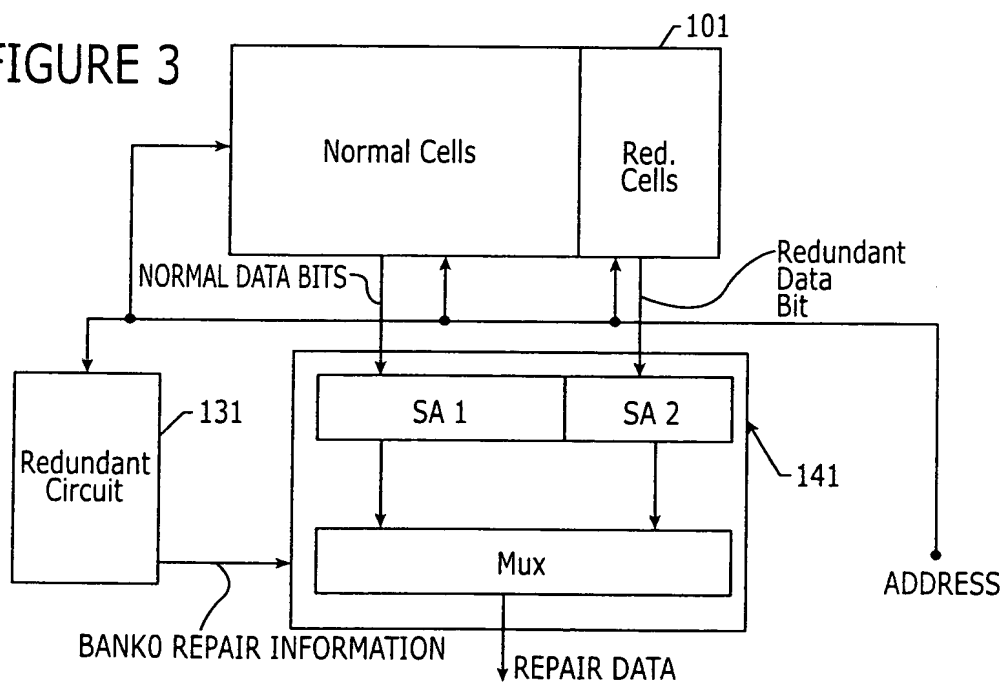
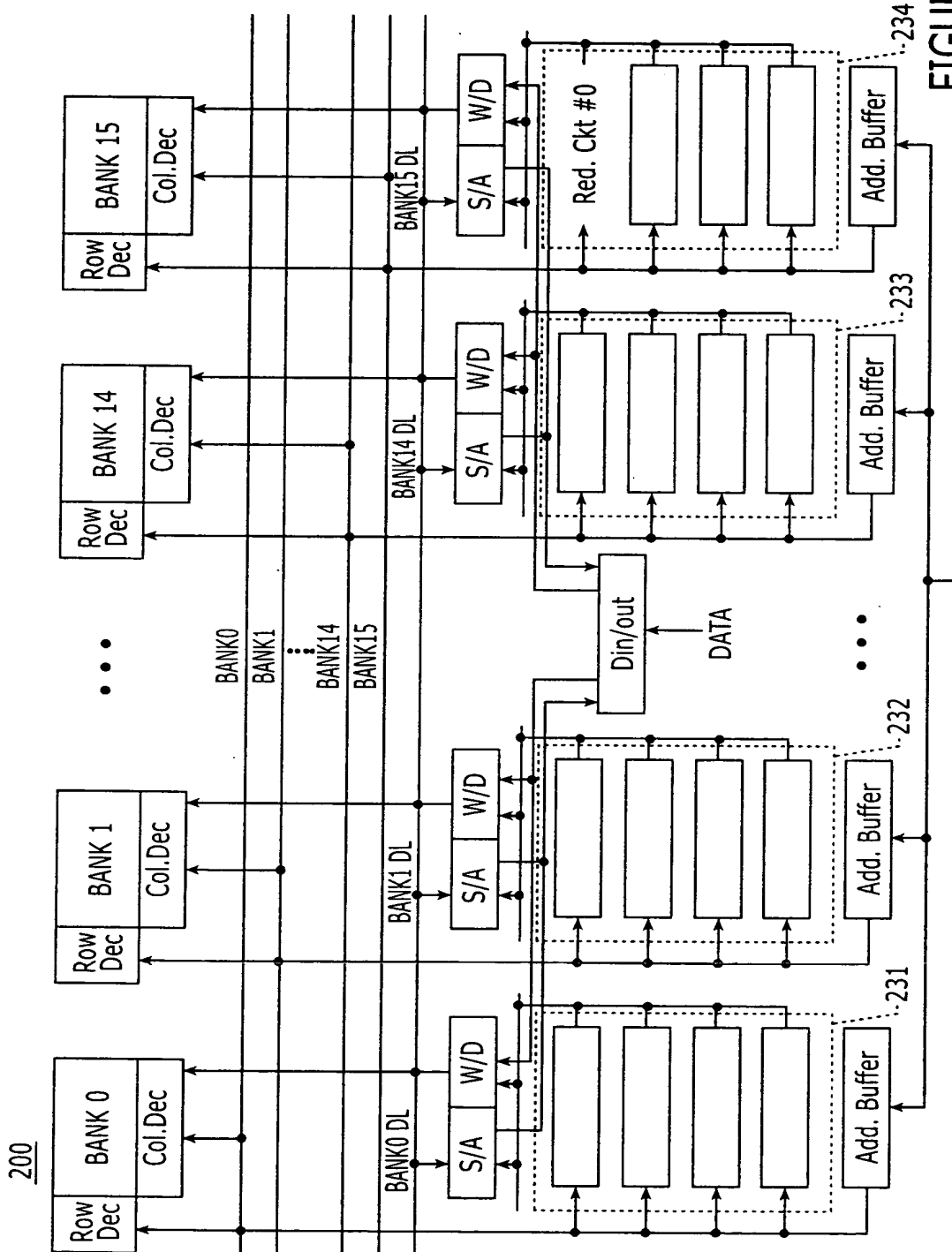


FIGURE 3





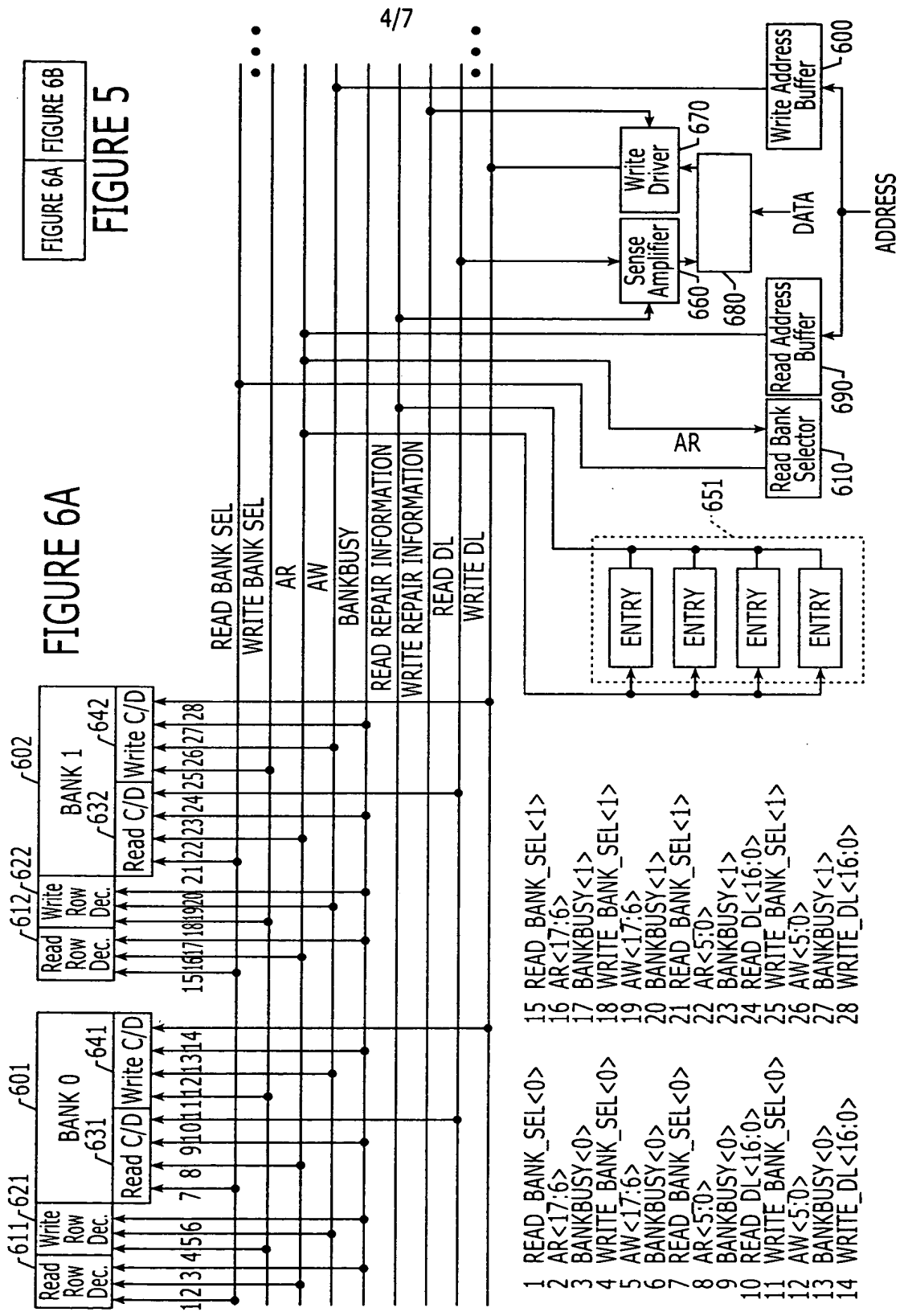


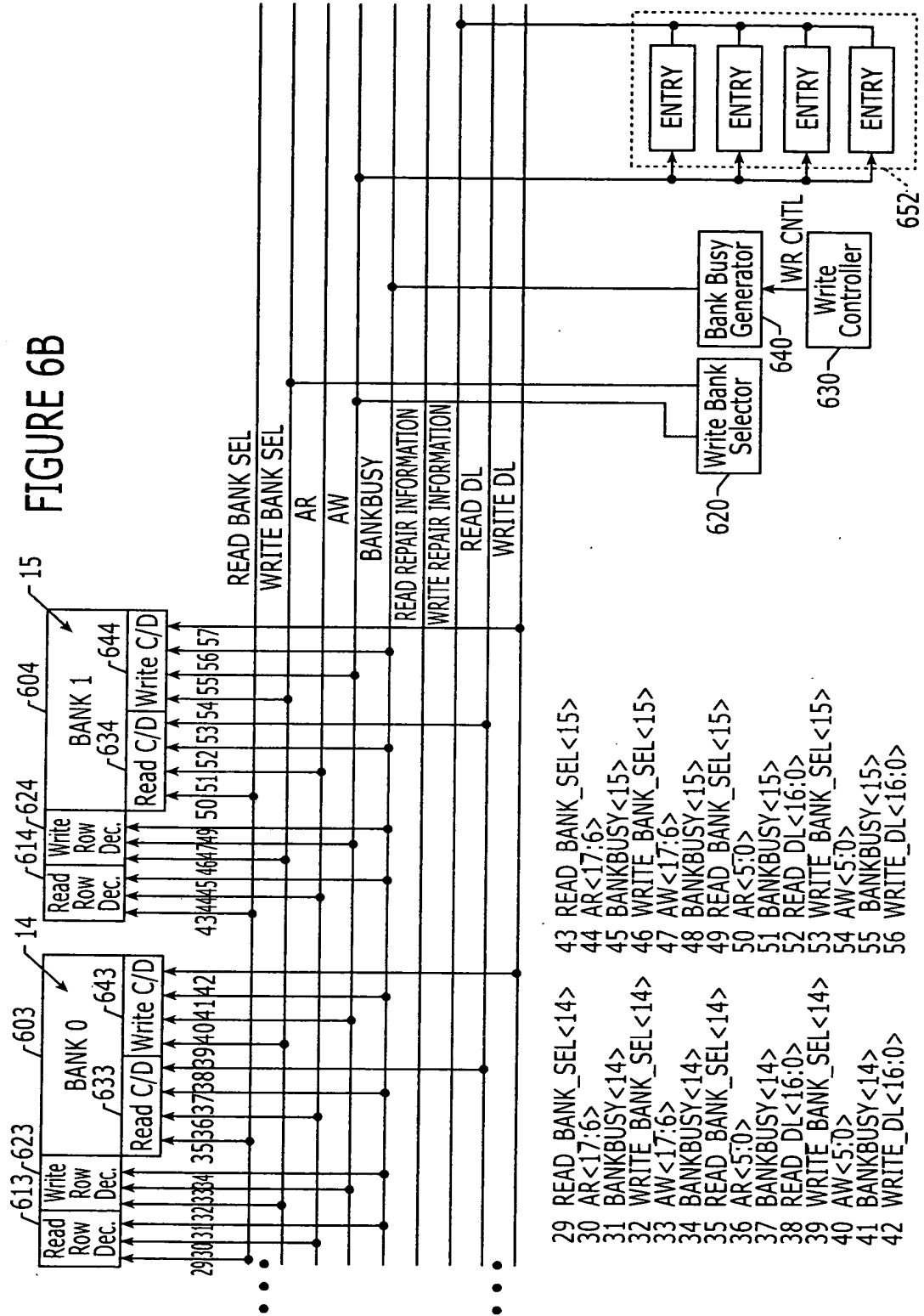
FIGURE 6A

FIGURE 6B

FIGURE 5

- 1 READ_BANK_SEL<0>
- 2 AR<17:6>
- 3 BANKBUSY<0>
- 4 WRITE_BANK_SEL<0>
- 5 AW<17:6>
- 6 BANKBUSY<0>
- 7 READ_BANK_SEL<0>
- 8 AR<5:0>
- 9 BANKBUSY<0>
- 10 READ_DL<16:0>
- 11 WRITE_BANK_SEL<0>
- 12 AW<5:0>
- 13 BANKBUSY<0>
- 14 WRITE_DL<16:0>
- 15 READ_BANK_SEL<1>
- 16 AR<17:6>
- 17 BANKBUSY<1>
- 18 WRITE_BANK_SEL<1>
- 19 AW<17:6>
- 20 BANKBUSY<1>
- 21 READ_BANK_SEL<1>
- 22 AR<5:0>
- 23 BANKBUSY<1>
- 24 READ_DL<16:0>
- 25 WRITE_BANK_SEL<1>
- 26 AW<5:0>
- 27 BANKBUSY<1>
- 28 WRITE_DL<16:0>

FIGURE 6B



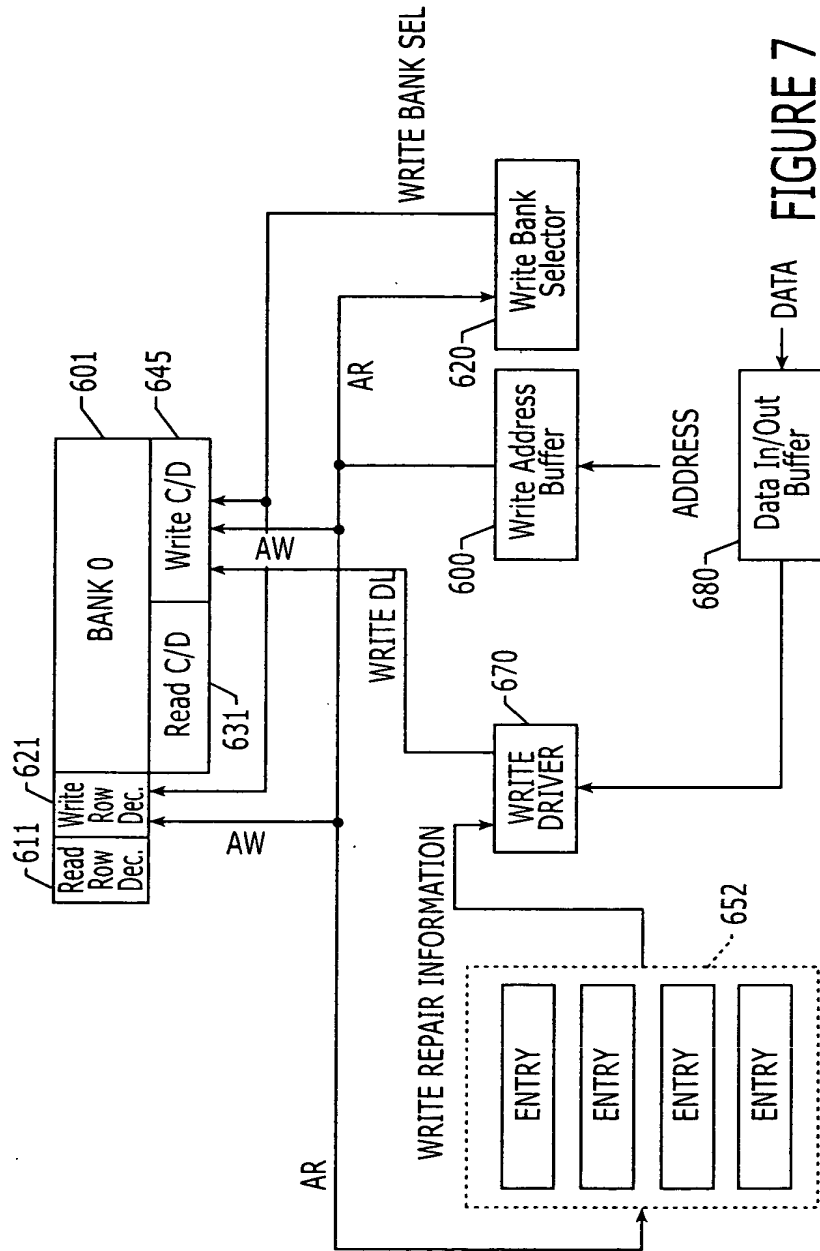


FIGURE 7

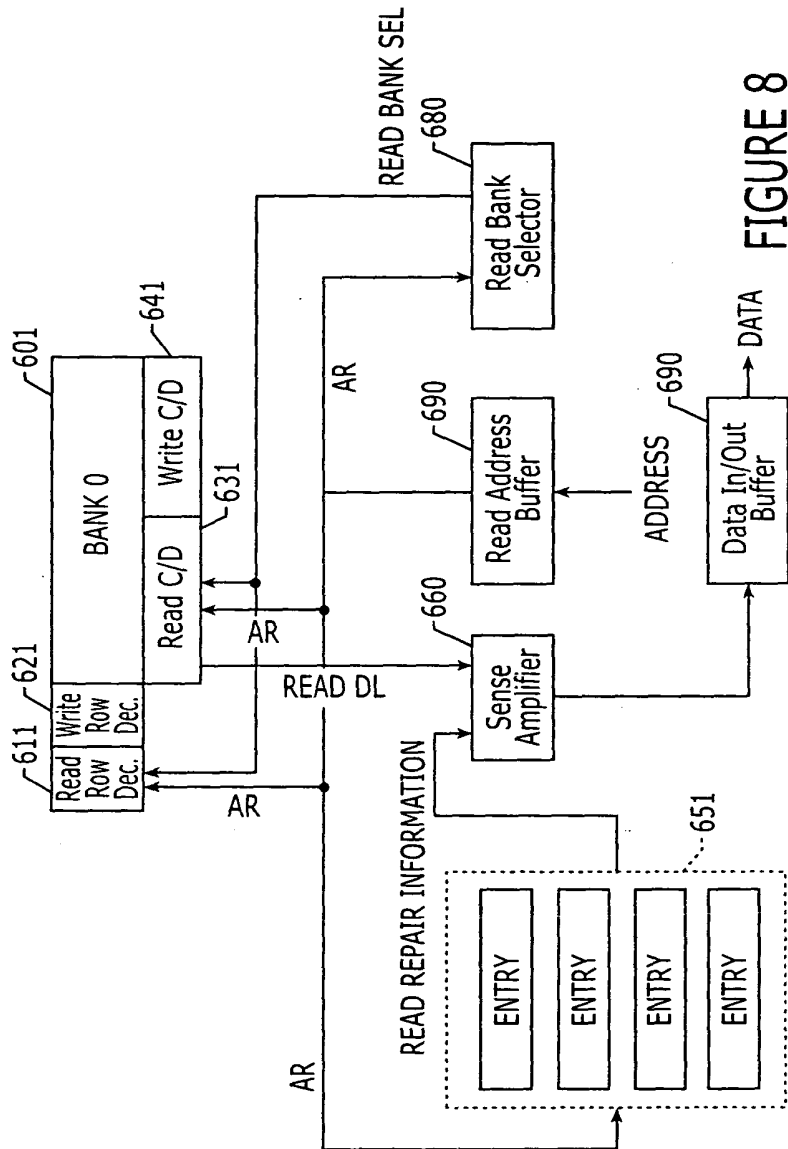


FIGURE 8